

Figure 17 is a diagram illustrating a length of coaxial cable as a kind of memory device for actual reflected signals; and

Figure 18 is a diagram illustrating a microstrip transmission line with capacitive stub branches to slow down the effective velocity. --

IN THE CLAIMS

Please amend the claims as follows.

Claim 10, line 1, delete "any one of claim 1 to 6, 8 or 9," and substitute therefor --claim 8,--.

Claim 11, line 1, delete "any one of claims 1 to 6, or 8 to 10," and substitute therefor --claim 8,--.

Claim 12, line 1, delete "any one of claims 1 to 6, or 8 to 11," and substitute therefor --claim 8,--.

Claim 13, line 1, delete "any one of claims 1 to 6, or 8 to 12," and substitute therefor --claim 8,--.

Claim 14, line 1, delete "any one of claims 1 to 6, or 8-13," and substitute therefor --claim 8,--.

Claim 16, line 1, delete "any preceding claims" and substitute therefor --claim 1--.

Claim 19, line 1, delete "16, 17 or 18," and substitute therefor --16--.

Claim 21, line 1, delete "or claim 20".

Claim 22, line 1, delete "20 or 21,".

Claim 24, line 1, delete "any one of claims 18 to 22," and substitute therefor --claim 18,--.

Claim 25, line 1, delete "any one of claims 18 to 23," and substitute therefor --claim 18,--.

Claim 27, line 1, delete "claim 26 with claim 23" and substitute therefor --claim 26,--.

30. (Amended) Method according to [claim 28 or] claim 29, wherein [the] each excursion[s] are as claimed in any one of claims 20 to 23] of each of the binary signal forms is opposite to the corresponding excursion of the other.

31. (Amended) Method according to claim 30, wherein the associated component [is as claimed in claim 26 or claim 27] has a voltage medial of the excursions.

Claim 32, line 1, delete "any one of claims 16 to 31," and substitute therefor --claim 16,--.

Claim 33, line 1, delete "directly or indirectly".

Claim 34, line 1, delete "directly or indirectly".

Claim 35, line 1, delete "or 34".

Claim 36, line 1, delete "any preceding claim" and substitute therefor --claim 1--.

Claim 37, line 1, delete "any preceding claim" and substitute therefor --claim 36--;

line 4, before "reflective state" insert --a--.

Claim 40, line 1, delete "38 or 39,".

Claim 41, line 1, delete "any one of claims 37 to 40," and substitute  
therefor --claim 37,--.

Claim 42, line 1, delete "any one of claims 37 to 41," and substitute  
therefor --claim 37,--.

**Claim 43,** line 1, delete "any one of claims 37 to 42," and substitute therefor --claim 37,--.

Claim 44, line 1, delete "any one of claims 37 to 43" and substitute therefor --claim 37,--.

45. (Amended) Method according to [any one of claims] claim 37 [to 43, with  
claim 25 or 28], wherein signal formats for the two binary values each have two  
successively oppositely directed voltage excursions and an associated component different  
from its excursions, and master(s) use strobe and reset pulses in controlling  
communications and slave or router units during said associated signal components and/or  
during gaps between bit signals.

Claim 46, line 1, delete "any preceding claim, wherein an in" and substitute therefor --claim 1, wherein an--.

47. (Amended) Signalling system [or apparatus for use in carrying out method according to any preceding claim] comprising:

transmitting and first receiving means for sending signals and receiving meaningful signals resulting from selective deliberate reflection of the transmitted signals; and  
second receiving means for detection of serial meaningful signal components of said transmitted signals, the second receiving means having re-transmission means including signal reflection means operable selectively as to which of different reflections is applied

individually to serial signal components of said transmitted and detected signals in order to produce serial meaningful components of said resulting signal.

48. (Amended) Signalling system or apparatus according to claim 47 [with claim 37], wherein coupling of a slave or router node to a transmission line implies a continuous conductive path therethrough along which DC or low frequency AC power can be passed along with signalling; and

wherein communication by a said master unit with a said slave unit involves said master unit selecting said slave unit according to reflective state thereof.

Claim 49, line 1, delete "or claim 48".

Please add the following new claims 51-54.

--51. Method of signalling wherein transmitting means sends signals and receiving means sends signals and receives meaningful signals resulting from selective deliberate reflection of the transmitted signals, wherein serial meaningful components of said resulting signals have different meanings according to which of different deliberate reflections is selected for and applied individually to serial meaningful components of said transmitted signals, and selection between said deliberate reflections is made individually for each said component.

52. Method of binary signalling wherein signal formats for different binary values each have two consecutively oppositely directed voltage excursions and an associated following component different from the excursions to serve in signal processing of those preceding associate excursions.

53. Method of binary signalling wherein signal formats for different binary values each have two consecutively opposite voltage excursions, wherein a following component